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Bib Data Sheet

CONFIRMATION NO. 6481

|                             |                                       |              |                        |                                  |
|-----------------------------|---------------------------------------|--------------|------------------------|----------------------------------|
| SERIAL NUMBER<br>10/528,160 | FILING DATE<br>03/17/2005<br><br>RULE | CLASS<br>257 | GROUP ART UNIT<br>2818 | ATTORNEY DOCKET<br>NO.<br>Q86110 |
|-----------------------------|---------------------------------------|--------------|------------------------|----------------------------------|

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a 371 of PCT/JP03/14731 11/19/2003 ENT

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2002-335855 11/20/2002

ENT

|   |                         |                       |                     |                           |
|---|-------------------------|-----------------------|---------------------|---------------------------|
| Foreign Priority claimed<br><input checked="" type="checkbox"/> yes <input type="checkbox"/> no   | STATE OR<br><br>COUNTRY | SHEETS<br><br>DRAWING | TOTAL<br><br>CLAIMS | INDEPENDENT<br><br>CLAIMS |
| 35 USC 119 (a-d) conditions met<br><input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance | JAPAN                   | 18                    | 8                   | 2                         |
| Verified and Acknowledged<br>Examiner's Signature <i>[Signature]</i> Initials ENT   |                         |                       |                     |                           |

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## TITLE

Semiconductor package and laminated semiconductor package

|                                   |   |   |
|-----------------------------------|---|---|
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